

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. MI22-1757		SERIAL NO. 09/896,877	
<div style="border: 2px solid black; border-radius: 50%; padding: 10px; display: inline-block; transform: rotate(-15deg);"> <b>TYPE</b>  <b>JAN 17 2002</b>  <b>PATENT &amp; TRADEMARK OFFICE</b> </div>					<b>ART CITED BY APPLICANT</b> <small>(Use several sheets if necessary)</small>			
					APPLICANT Wendell P. Noble			
					FILING DATE June 29, 2001		GROUP 2812	
U.S. PATENT DOCUMENTS								
*Examiner. Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
	AA					<div style="transform: rotate(90deg);">             FEB - 4 2002              10:28 AM MAIL ROOM           </div>		
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						Yes	No	
	AM	0 720 221 A1	07/03/96	EPO				
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AR	Kuge et al., <i>SOI-DRAM Circuit Technologies For Low Power High Speed Multigiga Scale Memories</i> , IEEE Journal of						
		Solid State Circuits, April 1996, pp. 586-591.						
	AS	Suma et al., <i>An SOI-DRAM With Wide Operating Voltage Range by CMOS/SIMOX Technology</i> , IEEE Journal of						
		Solid State Circuits, November 1994, pp. 1323-1329.						
	AT	Bakeman et al., <i>A High Performance 16-Mb DRAM Technology</i> , 1990 Symposium on VLSI Technology Digest of						
		Technical Papers, Honolulu, HI, June 4-7, pp. 11-12.						
EXAMINER <i>Symon A. Lutz</i>				DATE CONSIDERED <i>8/26/02</i>				
<small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>								

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JTB	AR	Kohyama et al., <i>Buried Bit Line Cell for 64MB DRAMs</i> , IEEE 1990 Symposium on VLSI Technology, pp. 17-18.						
JTB	AS	Davari et al., <i>A Variable-Size Shallow Trench Isolation (STI) Technology With Diffused Sidewall Doping for Submicron CMOS</i> , IEDM Technical Digest, International Electron Devices Meeting, San Francisco, CA, Dec. 11-14, 1988, pp. 92-95.						
JTB	AT	<i>Buried Stud that Eliminates Substrate and Well Contact Requirements</i> , IBM Web Page, June 1996, 2 pgs.						
EXAMINER <i>Symon A. Kelly</i>				DATE CONSIDERED <i>8/24/02</i>				
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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. MI22-1757		PRIORITY SERIAL NO. 09/310,037	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Wendell P. Noble		PRIORITY FILING DATE May 11, 1999	
				PRIORITY GROUP 2812			
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*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
<i>JL</i>	AA	4,604,162	08/1986	Sobczak	156	657	—
<i>JL</i>	AB	4,700,461	10/1987	Choi et al.	437	416	—
<i>JL</i>	AC	5,011,783	04/1991	Ogawa et al.	437	19	—
<i>JL</i>	AD	5,214,603	05/1993	Dhong et al.	365	207	—
<i>JL</i>	AE	5,391,911	02/1995	Beyer et al.	257	522	—
<i>JL</i>	AF	5,539,229	07/1996	Noble, Jr. et al.	257	301	—
<i>JL</i>	AG	5,599,724	02/1997	Yoshida	437	40	—
<i>JL</i>	AH	5,608,248	03/1997	Ohno	257	306	—
<i>JL</i>	AI	5,763,931	06/1998	Sugiyama	257	506	—
<i>JL</i>	AJ	5,831,305	11/1998	Kim			—
<i>JL</i>	AK	5,846,854	12/1998	Giraud et al.	438	149	—
<i>JL</i>	AL	6,004,865	12/1999	Horiuchi et al.			—
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	Document Number	Date	Country	Class	Subclass	Translation	
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
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EXAMINER <i>Syneth Hilly</i>				DATE CONSIDERED <i>8/26/02</i>			
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PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
MI22-1757PRIORITY SERIAL NO.  
09/310,037LIST OF ART CITED BY APPLICANT  
(Use several sheets if necessary)APPLICANT  
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<i>CB</i>	AA	6,091,129	07/2000	Cleeves			
<i>CB</i>	AB	6,117,760	09/2000	Gardner et al.			
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## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

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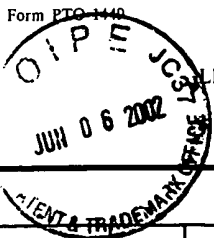
*G. H. Hardy*

DATE CONSIDERED

*8/26/02*

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Form PTO-140

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
M122-1757SERIAL NO.  
09/896,877LIST OF ART CITED BY APPLICANT  
(Use several sheets if necessary)APPLICANT  
Wendell P. NobleFILING DATE  
June 29, 2001GROUP  
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## U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<i>JS</i>	AA	6,274,919	08/14/01	Wada			
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							Yes	No
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## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

<i>JS</i>	AR	Article: SEPIA: A New Isolation Structure for Soft-Error-Immune LSI's (February 1993)
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EXAMINER

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*8/26/02*

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